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Not yet assigned

November 3, 2003

Steven R. Kleiman

Not yet assigned

Not yet assigned

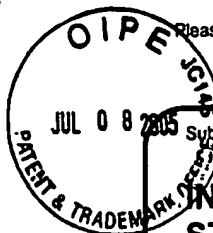
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<p>Substitute for form 1449A/PTO</p> <h2 style="margin: 0;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT</h2> <p style="margin: 0;">(use as many sheets as necessary)</p>			<p>Complete if Known</p> <table border="1" style="width: 100%; border-collapse: collapse;"><tr><td style="width: 50%;">Application Number</td><td style="width: 50%;">10/700,227</td></tr><tr><td>Filing Date</td><td>November 3, 2003</td></tr><tr><td>First Named Inventor</td><td>Steven R. Kleiman et al.</td></tr><tr><td>Group Art Unit</td><td>Not yet assigned</td></tr><tr><td>Examiner Name</td><td>Not yet assigned</td></tr><tr><td>Attorney Docket Number</td><td>112056-0143</td></tr></table>		Application Number	10/700,227	Filing Date	November 3, 2003	First Named Inventor	Steven R. Kleiman et al.	Group Art Unit	Not yet assigned	Examiner Name	Not yet assigned	Attorney Docket Number	112056-0143
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Sheet	1	of	6													

U.S. PATENT DOCUMENTS						
Examiner Initials *	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
CP	1	Re. 34,100		Hartness	10-13-1992	
CP	2	3,876,978		Bossen et al.	04-08-1975	
CP	3	4,092,732		Ouchi	05-30-1978	
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CP	16	4,817,035		Timsit	03-28-1989	
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CP	35	5,202,979		Hillis et al.	04-13-1993	

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Substitute for form 1449A/PTO

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Sheet	2	of	6
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Application Number	10/700,227
Filing Date	November 3, 2003
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OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
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CP	60	ANVIN, PETER H, "The Mathematics of RAID 6," December 2004	
CP	61	Auspex 4Front NS2000, System Architecture, Network-Attached Storage For a New Millennium, Auspex Engineering Technical Report 24, January 1999	
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CP	67	CHEN, PETER M., ET AL., Maximizing Performance in a Striped Disk Array, Proc. 1990 ACM SIGARCH 17th Intern. Symp. on Comp. Arch., Seattle, WA, May 1990, pp. 322-331.	
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Examiner Signature	Chuse Rees		Date Considered 1/12/06

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CP	73	GIBSON, GARTH A., ET AL., <i>Coding Techniques for Handling Failures in Large Disk Arrays</i> , Technical Report UCB/CSD 88/477, Computer Science Division, University of California, (July, 1988.)	
CP	74	GIBSON, GARTH A., ET AL., <i>Failure Correction Techniques for Large Disk Arrays</i> , In Proceedings Architectural Support for Programming Languages and Operating Systems, Boston, Apr. 1989, pp 123-132	
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CP	83	JOHNSON, THEODORE, ET AL, <i>Tape Group Parity Protection</i> , IEEE Symposium on Mass Storage, pp. 72-79, March 1999	
CP	84	KATZ, RANDY H. ET AL., <i>Disk System Architectures for High Performance Computing</i> , undated	
CP	85	KENT, JACK ET AL., <i>Optimizing Shadow Recovery Algorithms</i> , <i>IEEE Transactions on Software Engineering</i> , 14(2):155-168, Feb. 1988.	

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CP	86	KIM, MICHELLE Y., <i>Synchronized Disk Interleaving</i> , IEEE Transactions on Computers, C-35(11):978-988, November 1986	
CP	87	KIM, MICHELLE, ET AL., <i>Asynchronous Disk Interleaving Approximating Access Delays</i> , IEEE Transactions on Computers, vol. 40, no.7, July 1991, pp. 801-810.	
CP	88	LAWLOR, F. D., <i>Efficient Mass Storage Parity Recovery Mechanism</i> , IBM Technical Disclosure Bulletin 24(2):986-987, July 1981	
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CP	90	LI, DON, ET AL., <i>Authors' Reply</i> , IEEE Transactions on Communications, 46:575, May 1998.	
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CP	92	MEADOR, WES E., <i>Disk Array Systems</i> , Proceedings of COMPCON, 1989, pp. 143-146	
CP	93	NG, SPENCER, ET AL., <i>Trade-Offs Between Devices and Paths in Achieving Disk Interleaving</i> , IEEE International Symposium on Computer Architecture, 1988, pp. 196-201	
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CP	96	PATEL, ARVIND M., <i>Adaptive Cross-Parity (AXP) Code for a High-Density Magnetic Tape Subsystem</i> , IBM Technical Disclosure Bulletin 29(6):546-562, November 1985	
CP	97	PATTERSON, D., ET AL., <i>A Case for Redundant Arrays of Inexpensive Disks (RAID)</i> , Technical Report, CSD-87-391, Computer Science Division, Electrical Engineering and Computer Sciences, University of California at Berkeley (1987)	
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CP	99	PATTERSON, DAVID A., ET AL., <i>Introduction to Redundant Arrays of Inexpensive Disks (RAID)</i> . In IEEE Spring 89 COMPCON, San Francisco, IEEE Computer Society Press, February 27 - March 3, 1989, pp. 112-117	
CP	100	STORAGESUITE "Performance Without Compromise: The Virtual Storage Architecture," catalogue, 1997	
CP	101	REDDY, A. L. NARASIMHA, ET AL., <i>An Evaluation of Multiple-Disk I/O Systems</i> , IEEE Transactions on Computers, Vol. 38, No 12, December 1989, pp. 1680 - 1690.	
CP	102	SCHULZE, MARTIN E., <i>Considerations in the Design of a RAID Prototype</i> , Computer Science Division, Department of Electrical Engineering and Computer Sciences, Univ. of CA, Berkley, August 25, 1988	
CP	103	SCHULZE, MARTIN., ET AL., <i>How Reliable is a RAID?</i> , Proceedings of COMPCON, 1989, pp. 118-123	
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CP	108	TWETEN, DAVID, <i>Hiding Mass Storage Under UNIX: NASA's MSS-H Architecture</i> , IEEE Symposium on Mass Storage, pages 140-145, May 1990	
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